

### **ABSTRACT OF THE DISCLOSURE**

5       An integrated circuit is provided with multiple data processing components  
associated with respective sources which generate trace data streams. A reference  
timestamp generator is provided and the trace data streams are annotated such that  
they are output off-chip together with reference timestamp data. Outputting the  
reference timestamp data together with the trace data streams enables temporal  
correlation between points in different trace data streams by trace analysis tools.

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[Figure 3]